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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several Sheets is Necessary)

Applicant:

Roy M. Carlson and David O. Erstad

Filing Date:

Group:

December 9, 2003

TBA

### **U. S. PUBLISHED PATENT APPLICATION DOCUMENTS**

Examiner Initials	No.	Document Number	Date	Name	Class	Subclass	Filing Date if
W	1.	2003/0091038 Å1	05/15/03	Hagedorn	370	359	Appropriate 11/09/01
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	3.	4,805,148	02/14/89	Diehl-Nagle et al.	365	154	11/22/85
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	6.	5,640,105	06/17/97	Sobelman et al.	326	36	09/10/96
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	36.	Sobelman, Gerald E. and Karl Fant, "CMOS Circuit Design of Threshold Gates with Hysteresis,"  Pages 1 - 5
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